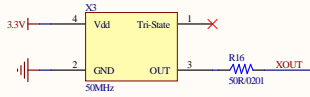
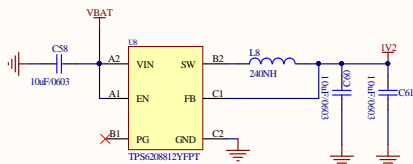


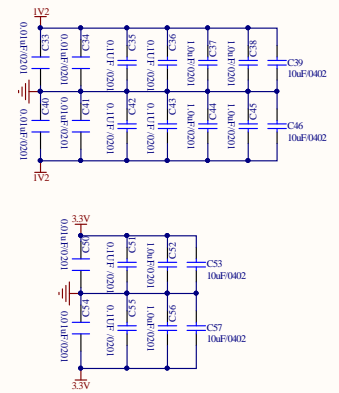
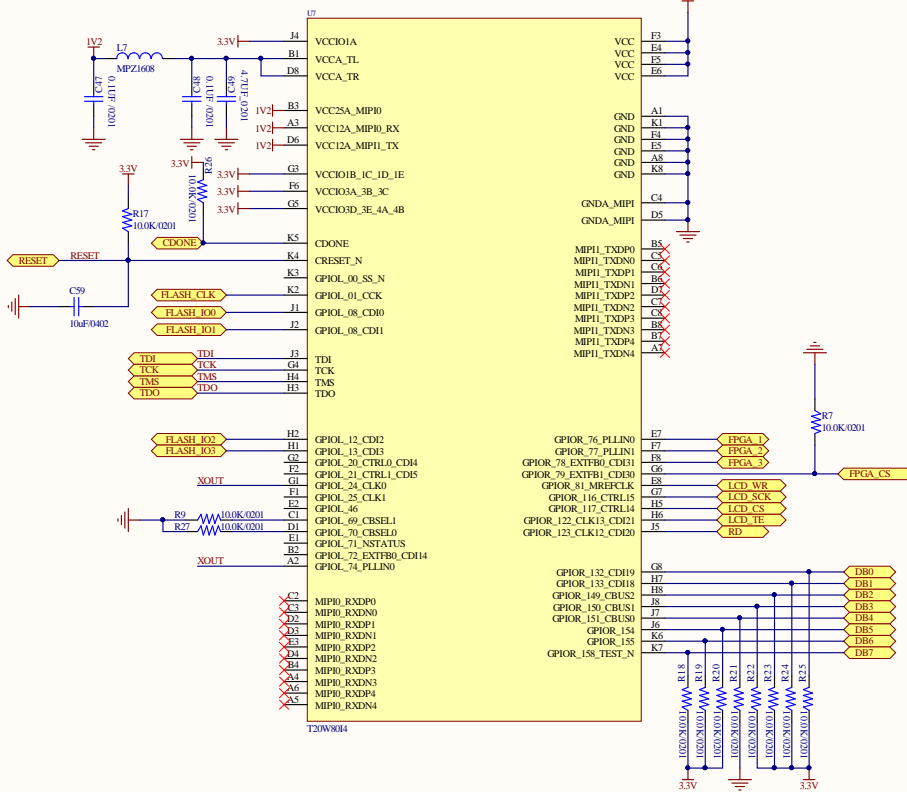
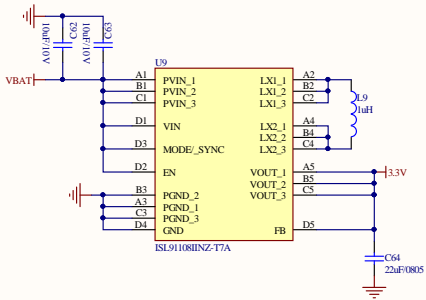
Oscillator



FPGA 1V2



FPGA 3V3



Title:		
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